



NARAYANA ENGINEERING COLLEGE :: NELLORE

(AUTONOMOUS)

Permanently affiliated to JNTUA Ananthapuramu, Approved by AICTE, Accorded 'A' grade by Govt. of AP, Recognized by UGC 2(f) & 12(B), ISO 9001:2015 certified Institution, Approved with 'A+' Grade by NAAC



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Report of FDP on "Latest Trends in VLSI Design and Implementation using EDA Tools"

Department of Electronics & Communication Engineering, Narayana Engineering College, Nellore, organized a "FDP on Latest Trends in VLSI Design and Implementation using EDA Tools". The FDP was conducted on the following dates: 06 – 04 – 2021 to 10 – 04 – 2021. The total number of participants attended the faculty development program is 30. The purpose of this FDP is to bring together researchers & PG students from Academia and Engineers & Scientists from industry and R&D institutes to have discussions on the latest trends in VLSI Design and Implementation using EDA Tools.

OBJECTIVES OF FDP:

- Providing a clear & thorough understanding on fundamental and application areas of VLSI Design.
- Providing an in-depth knowledge in use of various tools used in VLSI Design.
- Providing example of architectural design such as CLK, RST etc..
- Hands on experience on EDA Tools.



Inaugural function of the Online FDP

The inaugural function of the workshop started with a welcome address by Dr. K Murali, HOD-ECE and he presented the theme of the FDP and said that VLSI Design using EDA Tools is playing an important role in various applications. He addressed the participants and said that FDPs will help for the growth of the faculty in all facets of their professional lives. FDP will provide resources which will help the faculty to develop as scholars, to publish and share at professional conferences, and to improve their performance in the class room.



Resource Person addressed the participants about the VLSI Design using EDA Tools

The following member is the resource persons for the programme.

DR VIKRAM KUMAR PUDI

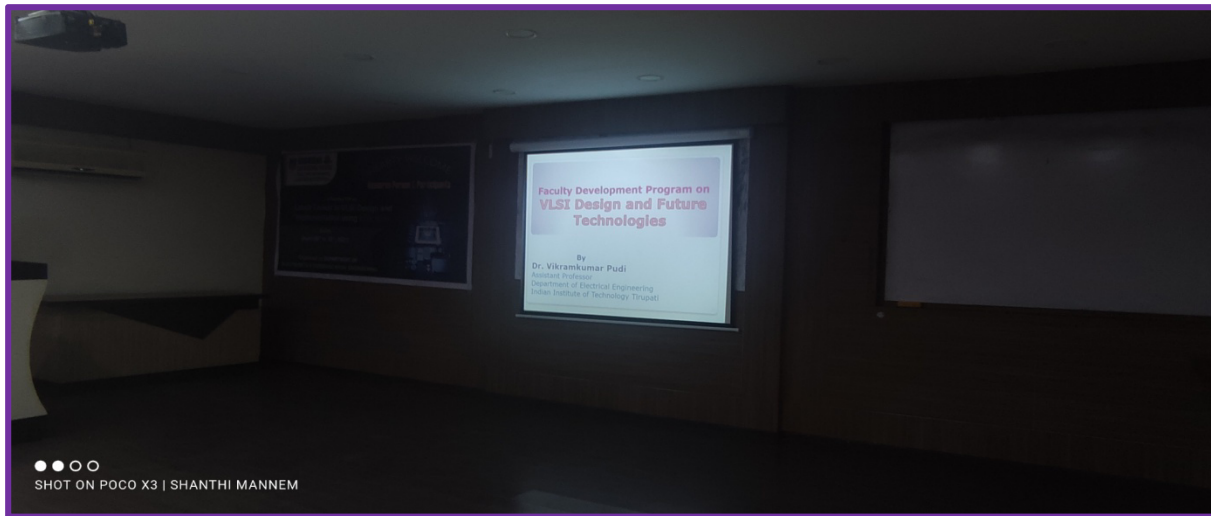
Assistant Professor – ECE,

Department of Electrical Engineering,

Indian Institute of Technology, Tirupathi.

The following topics covered by the resource person:

- Latest trends in VLSI design
- Introduction to various tools used in VLSI-Cadence virtuoso, Synopsis
- Mentor Graphics
- Xilinx, Tanner, Electric
- Architectural design such as CLK, RST, DATA_IN, DATA_OUT



Resource Person addressed the participants about the VLSI Design using EDA Tools



FDP on Latest Trends in VLSI Design and Implementation using EDA Tools

Expected outcomes:

- Opens new avenues for faculty members to pursue research in VLSI Design and Implementation.
- Detailed knowledge of various tools of VLSI Design.
- Importance of VLSI Design and Implementation in today's trends of life.